Drawings - Sheet 1 of 6

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(503) 227-5631 Inventor: Haliberg, et al. Tele:

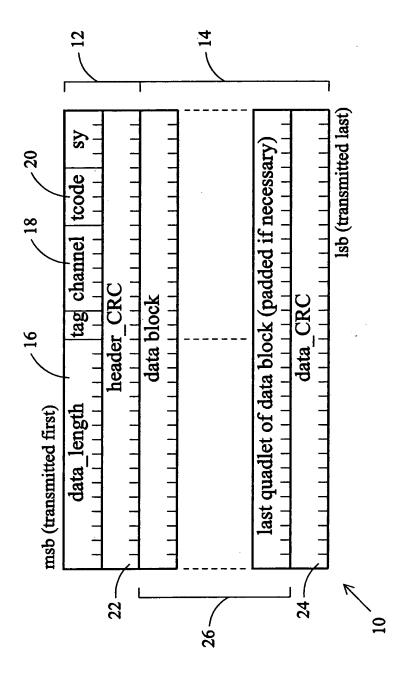


FIG. 1 (PRIOR ART)

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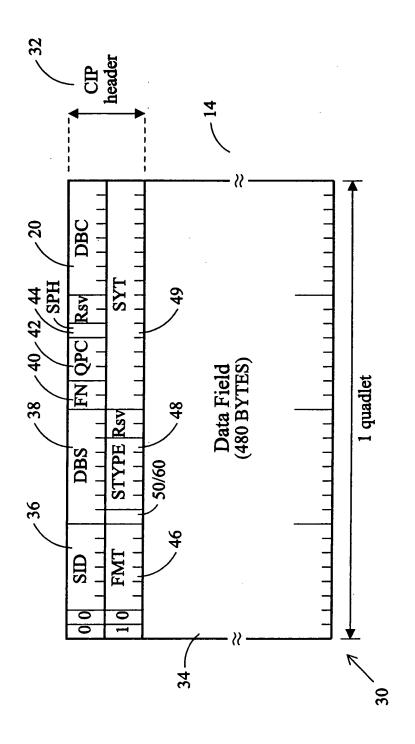


FIG. 2 (PRIOR ART)

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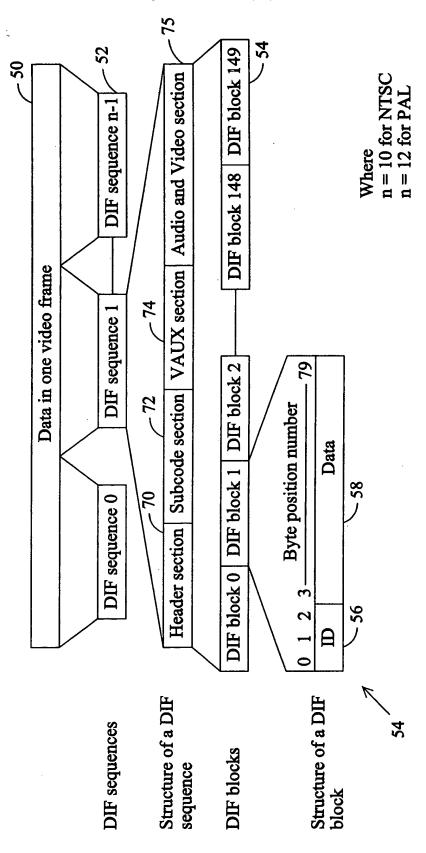


FIG. 3 (PRIOR ART)

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Inventor: Hallberg, et al. (503) 227-5631

54	· · · · · · · · · · · · · · · · · · ·										
	<i>—</i>	V13 V14	3	V43 V44 52 53	V58 V59 68 69	V73 V74 88	V88 V89 100 101	V102V103 V104	07 V108 V109 V110 V111 V112 V113 V114 V115 V116 V117 V118 V119	122 V123 V124 V125 V126 V128 V129 V130 V131 V132 V133 V134 V132 V133 V134 V134 V134 V134 V138 V139 V136 V141 V142 V143 V144 V145 V146 V147 V148 V149 FIG. 4 V148 V149 V149	
		V12 1	V27	V42 7	V57 67	V72 1	V87	V102V 115	V117V 131	V132V 147	
		V111	V26 34	V41 50	V56 66	V71 82	786 98	V101 114	V116 130	V131 146	
		V10	V25 33	V40 49	V55 65	V70 81	V85 97	V100 V101 113 114	V115 129	V130 145	
		76	V24 32	V39	V54 64	08 80	V84 96	V99 112	V114 128	144	
:		V8 15	V23	V38 47	V53 63	89A 79	V83 95	V98 111	V113 127	V128 143 D T	
		V7 14	V22 30	V37 46	V52 62	V67	V82 94	V97	V112 126	142 142 4	ζ.
		V6	V21 29	V36 45	V51 61	V66	V81 93	V96 109	V111 125	5 V126 0 141 FIG	5
- 74		V5	V20 28	V35	V50 60	V65	V80 92	V95 108	V110 124	24V125 V126 V127 V128 39 140 141 142 143 FIG. 4 (DDIOD ADT)	7
7	VA2	V4	V19	V34 43	V49 59	V64 75	V79 91	V94 107	V109 123	V124	ナ
	VA1	V3	V18 26	V33	V48 58	V63	V78 90	V93 106	V108 122	V123	
	VA0	V2 9	V17 25	V32	V47 57	V62 73	V77 89	V92 105			
	SC1	× \(\lambda \)	V16 24	V31 40	V46 56	V61 72	V76 88	V91 104	V105 V106 V 119 120 1	V120 V121 V 135 136 1	
	SC0	7	V15	V30 39	V45 55	V60 71	V75 87	V90 103	V105 119	V120 135	
72 –	0 H	A0	A1	A2 38	A3	A4 70	A5 86	A6 102	A7 118	A8 134	
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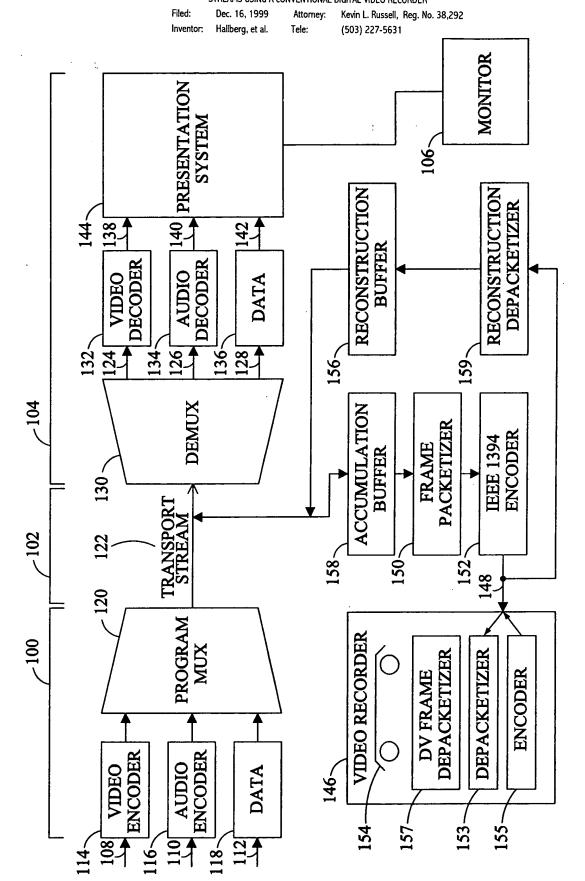


FIG. 5

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